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APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/784,629	02/15/2001 David D. Wu		2000.032100/TT3633 2717			
23720	7590 10/21/2003	10/21/2003 EXAMINER				
	S, MORGAN & AMERSO	CAO, PHAT X				
	IMOND, SUITE 1100 TX 77042	ART UNIT	PAPER NUMBER			
			2814			
			DATE MAILED: 10/21/2003			

Please find below and/or attached an Office communication concerning this application or proceeding.

	A				Ac			
	Office Action Summary	Applicatio	n No.	Applicant(s)				
ه. العر		09/784,629	9	WU ET AL.				
		Examin r		Art Unit				
		Phat X. Ca	0	2814				
Period fo	- The MAILING DATE of this communication app	pears on the	cover sh et with the co	orrespond nce ad	dress			
	ORTENED STATUTORY PERIOD FOR REPLY	Y IS SET TO	EXPIRE 3 MONTH(S	S) FROM				
THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status								
1)[\implies]	Responsive to communication(s) filed on <u>05 A</u>	August 2003	١.					
2a)⊠	,	nis action is						
3)								
•	on of Claims							
•	Claim(s) <u>1-47</u> is/are pending in the application							
	4a) Of the above claim(s) 21-46 is/are withdraw	vn from con	sideration.					
5)	Claim(s) is/are allowed.							
6)⊠	Claim(s) <u>1, 7, 11, 17, 47</u> is/are rejected.							
7)⊠	Claim(s) <u>2-6,8-10,12-16 and 18-20</u> is/are object	cted to.						
•	Claim(s) are subject to restriction and/o	r election re	quirement.					
	on Papers							
•	The specification is objected to by the Examine		-hi-stadta bytho Ever	ninor				
10)[1	The drawing(s) filed on is/are: a)☐ acception acception acception to the specific acception acc							
11) 3	• • • • • • • • • • • • • • • • • • • •				er			
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner. If approved, corrected drawings are required in reply to this Office action.								
12) The oath or declaration is objected to by the Examiner.								
Priority under 35 U.S.C. §§ 119 and 120								
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).								
a) All b) Some * c) None of:								
۵٫۱	1. Certified copies of the priority documents have been received.							
	2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage								
application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.								
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).								
 a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121. 								
Attachment(s)								
2) Notice	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449) Paper No(s) _	·		(PTO-413) Paper No Patent Application (PT				

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DETAILED ACTION

Election/Restrictions

1. This application contains claims 21-46 drawn to an invention nonelected with traverse in Paper No. 3. A complete reply to the final rejection must include cancellation of nonelected claims or other appropriate action (37 CFR 1.144) See MPEP § 821.01.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in-
- (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).
- 3. Claims 1, 7, 11, 17 and 47 are rejected under 35 U.S.C. 102(e) as being anticipated by Yu (US. 6,255,175).

With respect to claim 1, Yu (Fig. 4) discloses a method comprising: forming a gate dielectric 208 above a surface of the substrate 102; forming a doped-poly gate structure 206 above the gate dielectric, the doped-poly gate structure 206 having an edge region; and forming a first dopant-depleted region 232 or 234 in the edge region of the doped-poly gate structure adjacent the gate dielectric and a second dopant-depleted region 212 in the substrate 102 under the edge region of the doped-poly gate structure 206.

With respect to claim 11, Yu (Figs. 4-6) discloses a method comprising: forming a gate dielectric 208 above a surface of a substrate 102; forming a doped-poly gate

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structure 206 above the gate dielectric, the doped-poly gate structure 206 having an edge region; forming a source/drain extension 252 adjacent the doped-poly gate structure; and forming a dopant-depleted poly region 232 in the edge region of the doped-poly gate structure adjacent the gate dielectric and a dopant-depleted-SDE region 212 in the substrate 102 under the edge region of the doped-poly gate structure 206.

With respect to claim 47, Yu (Fig. 4) discloses a method, comprising: forming a gate dielectric 208 above a surface of a semiconductor substrate 102; forming a doped-poly gate structure 206 above the gate dielectric, the doped-poly gate structure 206 having an edge region; and forming a first dopant-depleted region 232 in the edge region of the doped-poly gate structure adjacent the gate dielectric and a second dopant-depleted region 212 in the substrate 102 under the edge region of the doped-poly gate structure 206 by: implanting a counter-dopant into the edge region of the doped-poly gate structure adjacent the gate dielectric and implanting a dopant into the top surface of the substrate 102; and forming depleting dielectric spacers 240 adjacent the doped-poly gate structure.

With respect to claims 7 and 17, Yu (Fig. 5) further discloses depleting the edge region of the doped-poly gate structure 206 adjacent the gate dielectric and depleting the substrate 102 under the edge region of the doped-poly gate structure 206 by forming the depleting dielectric spacers 240.

Allowable Subject Matter

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4. Claims 2-6, 8-10, 12-16 and 18-20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art fails to disclose the combination of a method of forming a semiconductor device structure including the forming the first dopant depleted region including implanting a counter dopant into the edge region of the doped poly gate structure, and forming the second dopant depleted region including implanting the counter dopant into the substrate under the edge region of the doped poly gate structure.

Response to Arguments

5. Regarding claims 1, 7, 11, 17 and 47, Applicant argues that Yu's Fig. 4 does not disclose "a second dopant-depleted region in the substrate under the edge region of the doped-poly gate structure."

Applicant's argument is not persuasive because dopant-depleted region 212 or 214 is "a second dopant-depleted region" formed "in the substrate [102] under the edge region of the doped-poly gate structure [206]".

Conclusion

6. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not

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mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phat X. Cao whose telephone number is (703) 308-4917. The examiner can normally be reached on Monday - Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (703) 308-4918. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

PC October 17, 2003 PHAT X. CAO